In the Drawings

Figure 3 Add label "SAMPLING CLOCK 70" to line between VCO/INTEGRATOR 38 and DATA RECOVERY 40;

Add line labeled "LEADING CLOCK 72" between VCO/INTEGRATOR 38 and A RECOVERY 40;

Add bus between VCO/INTEGRATOR 38 and PHASE INTERPOLATOR 44;

Add label "EARLYB" to line between DATA RECOVERY 40 and PHASE SELECT CIRCUIT/FILTER 42;

Add label "PHCODE 82" to line between PHASE SELECT CIRCUIT/FILTER 42 and PHASE INTERPOLATOR 44; and

Add line labeled and "PHMUX" between PHASE SELECT CIRCUIT/FILTER 42 and PHASE INTERPOLATOR 44.

FIG. 2
(PRIOR ART)

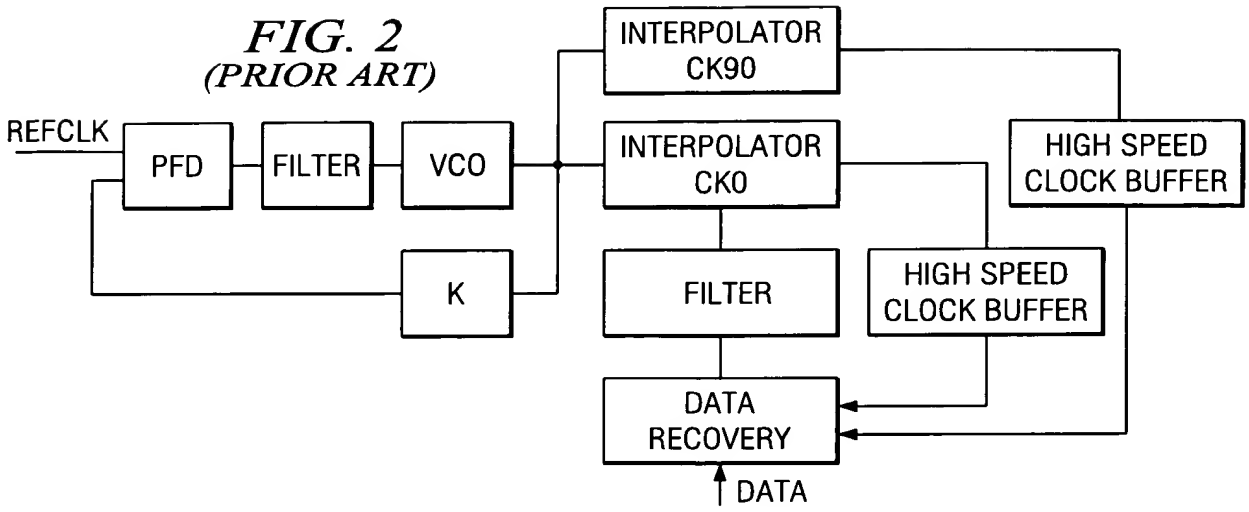

FIG. 3

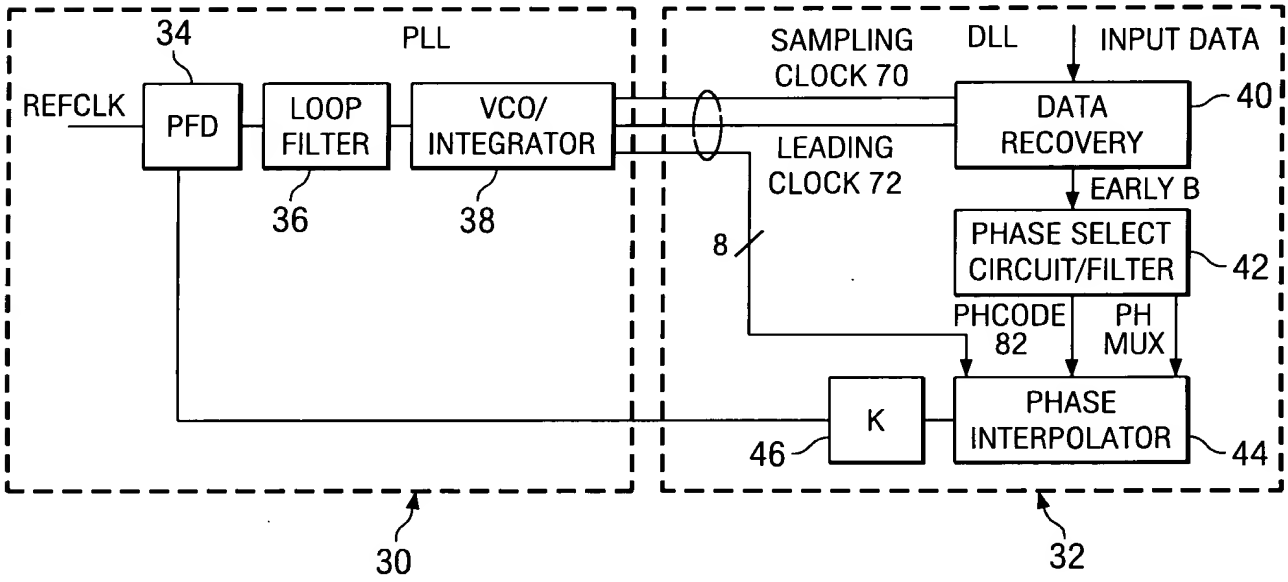

FIG. 4